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Performance of multi-threaded execution in a shared-**memory** multiprocessor
L Alkalaj, RV Bopanna - Proc. of 3rd Ann. IEEE Symp. on Parallel and Distributed ... - ieeexplore.ieee.org
... processor is a two level **hierarchical** system of ... the performance of the proposed
shared-**memory** architecture, we ... Memo, Labora- tory of **Computer** Science, November ...
Cited by 6 - Web Search - ieeexplore.ieee.org

Technical Report
MC Scheduling, P Allocation - informatik.uni-erlangen.de
... Frank Bellosa University Erlangen-Nürnberg Department of **Computer** Science IV ...
3.3.**Hierarchical** hash queues without aging ... **Memory** conscious user-level scheduling ...
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Towards a distributed **memory** implementation of Sisal
M Haines, APW Boehm, D Computing - 1991 - ieeexplore.ieee.org
... Matthew Haines* Wim Böhm **Computer** Science Department ... vec- tor [4, 16], **hierarchical**
mmory [30 ... distribution, parameter passing, and **dynamic memory** allocation [13 ...
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Computer-Assisted Generation of PVM/C++ Programs Using CAP.
BA Gennart, JT Gimenez, RD Hersch - PVM, 1996 - diwww.epfl.ch
... Both shared- and distributed-**memory** architectures are ... attempts to build
computer-aided parallelization ... thread : the thread description is **hierarchical**. ...
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The performance implications of locality information usage in shared-**memory** multiprocessors
F Bellosa, M Steckermeier - Journal of Parallel and Distributed Computing, 1996 - iits.uap.edu.ph
... Department of **Computer** Science IV, University of Erlangen-Nürnberg ... as a consequence
of non local **memory** accesses (see Fig. ... distributed **hierarchical** structures ...
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Management of heterogeneous parallelism on shared **memory** multiprocessors
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operation as it ... The simplest **dynamic** loop scheduling algorithm is called self ...
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Distributed management by delegation
GIS Goldszmidt, Y Yemini - 1996 - doi.ieeecs.org
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communications between DPISOVer shared **memory**. ... agent and a **hierarchical** manager ...
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JC Ueng, CK Shieh, SC Mac, AC Lai, TY Liang - IEICE Transactions on Information and Systems, 1999 -
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... **Thread management** in Cohesion can be divided into three main ... a **hierarchical** mechanism
for barriers, called a **hierarchical** barrier ... 3.4 **Dynamic Memory** Allocation ...

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[PS] In the Proceedings of the 11th Sympo sium on **Computer** Architecture and High
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P Kakulavarapu - cos.ufrj.br
... of Electrical and **Computer** Engineering Newark, USA amaral ... the ap- plication, called
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Overview of the JUMP-i, an MPP Prototype for General-Purpose Parallel Computations
T Kudoh, H Nakashima, H Nakajo - ieeexplore.ieee.org
... directory nsetlsod that combines a **hierarchical** fulimap method. ... implemented by
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... and examples which are rather **remote** from the ... The advent of the **computer** has
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NHF Beebe - math.utah.edu
... Compute [Mil93]. **Computer** [Was93]. ... Driven [GGL93, PY93]. **Dynamic** [CMMZ93, DFWM93]. ...
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RL Multicast - cs.umn.edu
... 5. **Dynamic** group membership. ... Taubman and Zakhor's 3D Subband Coding **system** is a ...
increasing performance gap between the processor and **memory system** may make ...
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R Taylor - cs.man.ac.uk
... take place is available from the head of Department of **Computer** Science. ... **memory**. ... that
they use the Jini **system** (see Section 3.4) which allows **dynamic** discovery ...
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... (ii) No candidate who has obtained a Third Division (or 'D' grade where result is
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Model Report for Christina River Basin, Pennsylvania, Delaware, and Maryland, High-Flow Nutrient and ...
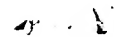USEP Agency, P Philadelphia - epa.gov
... 3-10 3.18 **Computer** Requirements ... to simulation of the hydraulics in any combined **system**
of open and ... XP-SWMM uses a self-modifying **dynamic** wave solution algorithm ...
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... for the design of ship **system** automation. ... and quantify their effects on human–**computer**
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SYSTEM . . . . When it is available, **dynamic** ...
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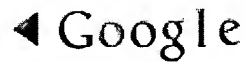
1 CHAPTER 1 INTRODUCTION..
E Routes - ub.uib.no
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A survey of design techniques for system-level dynamic ... - by Benini - 143 citations
Microsoft Repository Version 2 and the Open Information ... - by Bernstein - 53 citations

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... Structure Description DSI **Dynamic** Skeleton Interface ... DSM Distributed Shared **Memory**
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locality is achieved, resulting in very few main **memory**. accesses. On the other
hand, ... This appendix gives the examples for IML **API** functions. ...
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is presented in [9], resulting in a **hierarchical** mesh reduction technique with
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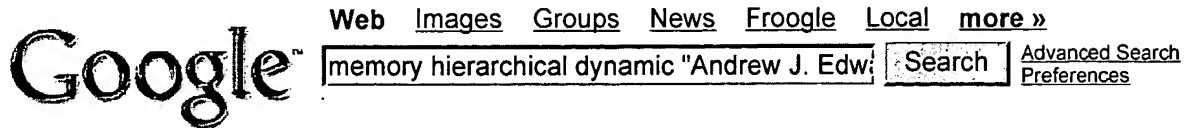Preferences

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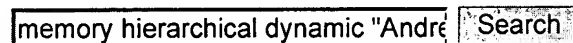
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